

#### POZNAN UNIVERSITY OF TECHNOLOGY

EUROPEAN CREDIT TRANSFER AND ACCUMULATION SYSTEM (ECTS)

#### **COURSE DESCRIPTION CARD - SYLLABUS**

Course name

Parallel processing [S1Inf1>PROW]

Course

Field of study Year/Semester

Computing 3/6

Area of study (specialization) Profile of study

general academic

Level of study Course offered in

first-cycle Polish

Form of study Requirements full-time compulsory

**Number of hours** 

Lecture Laboratory classes Other (e.g. online)

30 16 0

Tutorials Projects/seminars

0

Number of credit points

3,00

Coordinators Lecturers

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### **Prerequisites**

The student starting this course should have knowledge of the computer organization, algorithms and data structures, operating systems and programming in the C language.

# Course objective

Providing students with basic knowledge of parallel processing, including: models, computing systems, environments and languages, problems and methods of solving them. Developing students" skills in: solving tasks in the field of processing and optimization of processing in a parallel computing system, comparing the effectiveness of parallel processing carried out with the use of various environments and systems. Developing in students awareness of the need to use tools that allow for the assessment of processing efficiency in parallel processing systems (profilers), understanding the principles of their operation.

#### Course-related learning outcomes

## Knowledge:

The student has ordered, theoretically founded knowledge of parallel programming paradigms, parallel computer systems, parallel processing algorithms and their complexity evaluation.

The student has knowledge of the directions of development of architectures of parallel computer systems.

The student knows the methods, techniques and tools used to solve computer science tasks in the field of parallel programming.

#### Skills:

The student is able to properly plan and perform experiments to evaluate the effectiveness of parallel processing, interpret the results and correctly draw conclusions from them.

The student is able to solve problems in the field of parallel processing using appropriate experimental methods (OpenMP, Intel Vtune, Cuda, Cuda profiler).

The student has the ability to formulate parallel algorithms and their implementation in the OpenMP and CUDA environment.

The student is able to cooperate and work in a group by carrying out projects and research on the implementation and evaluation of the effectiveness of parallel algorithms.

#### Social competences:

The student is aware of the importance of the knowledge - regarding both hardware and software of parallel systems - needed in solving problems in the field of parallel processing optimization. The student understands the need to broaden the knowledge and skills resulting from technological progress in the field of parallel processing equipment.

## Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:

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Formative assessment:

- during lectures is based on answers to questions related to the material covered in previous lectures;
- during laboratory classes is based on assessment of the current progress and the results of the ongoing and finished tasks and projects .

Total assessment:

- verification of assumed learning objectives related to lectures is performed by assessment of the knowledge and skills during a final test consisting of close-ended questions of multiple-choice answers and hand assessed open-ended questions of a problem nature; examples of problem tasks are solved in the classroom; exemplary test task from previous years are available for students in a form of a list.
- verification of assumed learning objectives related to laboratory classes is based on an evaluation of student's knowledge necessary to prepare and carry out the lab tasks, monitoring students' activities during classes and evaluation of laboratory reports.

### Programme content

The program includes Internal parallelism of single processor computing systems, processor cache memory and coherency of cache in multiprocessor systems, spatial and temporal locality of data access, efficiency evaluation of parallel algorithms and parallel systems, parallel programming environments: OpenMP, CUDA.

#### Course topics

The purpose of this course is to provide information concernig:

- internal parallelism of general purpose single processor computing systems, pipelining and super scalar architecture:
- classifications and examples of parallel systems (multi core systems, GPU);
- processor cache memory and coherency of cache in multiprocessor systems;
- the importance of spatial and temporal locality of data access for processing efficiency;
- basics of efficiency evaluation of parallel algorithms and parallel systems, scalability, Amdahl and Gustafson low:
- parallel processing models (shared memory, message passing, data paralelism);
- parallel algorithms: a general method of algorithm design, problem partition techniques, task assignment methods (divisible task method, task graph embeddings, load balancing);
- parallel programming environments and standards: OpenMP, CUDA;
- tools for parallel processing efficiency evaluation: Intel Vtune, Nvidia Profiler;

- parallel algorithms for sorting, reduction, stencil computation, matrix multiplication, primary numbers. The lab-classes are focused on: practical exercises with software implementations (in OpenMP, CUDA environments) for selected problems in parallel multicore systems and GPU, data sharing in multicore architectures and efficiency evaluation of different approaches.

## **Teaching methods**

Lectures: presentation illustrated with examples on a black board.

Labs: presentation of work and use of parallel environments and tools; discussion of the results and parallel processing efficiency for experiments performed on code prepared by students in various systems.

### **Bibliography**

#### Basic

- 1. Wprowadzenie do obliczeń równoległych, Z. Czech, PWN, Warszawa, 2013.
- 2. Cuda w przykładach: wprowadzenie do ogólnego programowania procesorów GPU, J.Sanders, E.Kandrot, Helion, 2012.
- 3. Introduction to Parallel Computing, A.Grama, A.Gupta, G.Karypis, V.Kumar, Addison Wesley, 2003 Additional

Specifications and manuals for graphic cards, OpenMP, CUDA and Parallel Studio.

## Breakdown of average student's workload

	Hours	ECTS
Total workload	75	3,00
Classes requiring direct contact with the teacher	48	2,00
Student's own work (literature studies, preparation for laboratory classes/tutorials, preparation for tests/exam, project preparation)	27	1,00